

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	STRAINED SILICON NMOS DEVICES WITH EMBEDDED SOURCE/DRAIN
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Application Number :

Confirmation Number:

First Named Applicant: Dureseti Chidambarao

Attorney Docket Number: FIS920040012

Art Unit:

Examiner:

Search string: (6063675 or 6261911 or 6406951 or 6440807 or 6607948 or 6303450 or 6326664 or 6346732 or 6403434 or 6489206 or 6620664 or 6657223 or 20030146428 or 20030162348 or 20030219971 or 20030227029).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6063675	2000-05-16	Rodder			
	2	6261911	2001-07-17	Lee et al.			
	3	6406951	2002-06-18	Yu			
	4	6440807	2002-08-27	Ajmera et al.			
	5	6607948	2003-08-19	Sugiyama et al.			
	6	6303450	2001-10-16	Park et al.			
	7	6326664	2001-12-04	Chau et al.			
	8	6346732	2002-02-12	Mizushima et al.			
	9	6403434	2002-06-11	Yu			
	10	6489206	2002-12-03	Chen et al.			
	11	6620664	2003-09-16	Ma et al.			
	12	6657223	2003-12-02	Wang et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030146428	2003-08-07	Ma et al.			
	2	20030162348	2003-08-28	Yeo et al.			
	3	20030219971	2003-11-27	Cabral, Jr., et al.			

	4	20030227029	2003-12-11	Lochtefeld et al.			
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Signature

Examiner Name	Date